

REMARKS

Reconsideration of this application, as amended, is respectfully requested. The following remarks are responsive to the Office Action mailed May 5, 2004 and the Advisory Action mailed September 14, 2004.

Claim Rejections

Claims 15-18, 20, 21, 26, and 28-30 are rejected under 35 U.S.C. 102(b) as being anticipated by U.S. Patent No. 4,045,781 (hereinafter "Levy").

Claim 15 as presently amended claims interface circuitry within the memory controller that enables the system bus to interface with a number of different memories having a number of different signal quality requirements in relation to each other. Levy, on the other hand discloses memory modules 30-33 having memory control and timing circuitry 42 that enables the bus 40 to be interfaced **only** to a plurality of magnetic core storage elements 44, 45 (see Col. 6, Lns. 44-47). Levy does not, however, disclose a memory controller in the memory modules 30-33 having interface circuitry to interface the bus 40 to any other memory type having any other signal quality requirements than the magnetic core storage elements discussed in column 6, lines 44-47.

Although Levy does discuss that the memory control and timing circuitry 42 can interface the bus 40 to stacks 44, 45 having different **sizes** (Col. 18 Lns. 28-55), Levy does not teach or suggest that the memory control and timing circuitry 42 can interface the bus 40 to varying types of memory having varying types of signal quality requirements, as in presently amended claim 15. Indeed, one of ordinary skill in the art would not equate a memory devices storage capacity with the memory device's signal quality requirements.

Accordingly, it is respectfully asserted by Applicant that neither presently-amended claim 15, nor any dependent claims of claim 15, are anticipated by Levy and is in condition for allowance.

Claims 19, 22-25, 27, and 31 are rejected under 35 U.S.C. 103(a) as being unpatentable over U.S. Patent 4,045,781 to Levy et al.

In view of the above amendments and arguments regarding claim 15, it is asserted by Applicant that limitations within claims 19, 22-25, 27 and 31 are not taught or suggested by Levy, and are therefore not obvious in view of Levy. Accordingly, Applicant respectfully asserts that claims 19, 22-25, 27 and 31 are in condition for allowance.

Claims 18-23 are rejected under 35 U.S.C. 103(a) as being unpatentable over U.S. Patent 4,045,781 to Levy et al. in view of U.S. Patent 5,257,233 to Schaefer. In view of the above amendments and arguments regarding claim 15, it is asserted by Applicant that limitations within claims 19, 22-25, 27 and 31 are not taught or suggested by Levy nor Schaefer, and are therefore not unpatentable over Levy in view of Schaefer. Accordingly, Applicant respectfully asserts that claims 19, 22-25, 27 and 31 are in condition for allowance.

Claims 18-23 are rejected under 35 U.S.C. 103(a) as being unpatentable over U.S. Patent 4,045,781 to Levy et al. in view of U.S. Patent 5,036,493 to Nielsen. In view of the above amendments and arguments regarding claim 15, it is asserted by Applicant that limitations within claims 19, 22-25, 27 and 31 are not taught or suggested by Levy nor Nielsen, and are therefore not unpatentable over Levy in view of Nielsen.

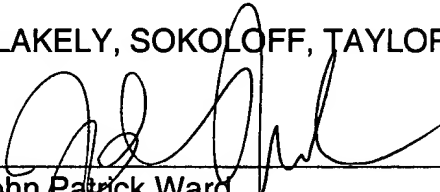
Accordingly, Applicant respectfully asserts that claims 19, 22-25, 27 and 31 are in condition for allowance.

Please charge any fees not covered by any checks submitted herewith to our Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: October 19, 2004



John Patrick Ward
Reg. No. 40,216

12400 Wilshire Blvd.
Seventh Floor
Los Angeles, CA 90025-1030
(408) 720-8300